

INolux 5050 RGB LED 4-Pin With Integrated IC IN-PI554FCH

| Official Product   | IN Part No. IN-PI554FCH | Customer Part No. |                | Data Sheet No. |
|--|-------------------------|-------------------|----------------|----------------|
| Preliminary Product  | *****                   | *****             |                | IN-PI554FCH    |
| Specifications are subject to change without notice. Data and drawings herein are copyrighted. |                         | Feb. 4, 2021      | Version of 2.5 | Page 1/19      |

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1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.

2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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#### **Product Specifications**

|              | Specification                       | Material                          | Quantity         |
|--------------|-------------------------------------|-----------------------------------|------------------|
| lv           | Red :550mcd typ.                    |                                   |                  |
|              | Green :1250mcd typ.                 |                                   |                  |
|              | Blue :300mcd typ.                   |                                   |                  |
|              | @12mA/ Ta= 25°C; Tolerance ±10%     |                                   |                  |
| λD           | Red :624nm typ.                     |                                   |                  |
|              | Green :524nm typ.                   |                                   |                  |
|              | Blue :466nm typ.                    |                                   |                  |
|              | @12mA/ Ta= 25° C; Tolerance ± 0.5nm |                                   |                  |
| Vf           | Red :1.8-2.2 V                      |                                   |                  |
|              | Green :2.8-3.2 V                    |                                   |                  |
|              | Blue :2.8-3.2 V                     |                                   |                  |
|              | @12mA/ Ta= 25°C; Tolerance ± 0.05V  |                                   |                  |
| Resin        | Clear                               | Epoxy Resin                       |                  |
| Carrier tape | EIA 481-1A specs                    | Conductive black tape             |                  |
| Reel         | EIA 481-1A specs                    | Conductive black                  | 1000pc/reel      |
| Label        | IN standard                         | Paper                             |                  |
| Packing bag  | 220x240mm                           | Aluminum laminated bag/ no-zipper | One reel per bag |
| Carton       | IN standard                         | Paper                             | Non-specified    |
|              |                                     | •                                 | •                |

Others:

Each immediate box consists of 5 reels. The 5 reels may not necessarily have the same lot number or the same bin

combinations of Iv,  $\lambda_D$  and Vf. Each reel has a label identifying its specification; the immediate box consists of a product label as well.

#### ATTENTION: Electrostatic Discharge (ESD) protection



The symbol to the left denotes that ESD precaution is needed. ESD protection for GaP and AlGaAs based chips is necessary even though they are relatively safe in the presence of low static-electric discharge. Parts built with AlInGaP, GaN, or/and InGaN based chips are **STATIC SENSITIVE devices**. ESD precaution must

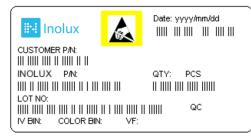
be taken during design and assembly.

If manual work or processing is needed, please ensure the device is adequately protected from ESD during the process.

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# **Label Specifications**



#### INolux P/N:

# IN-PI554FCH-XXXX

| Product            | Package                       | Color      | Customer Code          |
|--------------------|-------------------------------|------------|------------------------|
| IN:                | PI55:                         | FCH:       | XXXX:                  |
| INolux Corporation | 5.0 (L) x 5.0 (W) x1.6 (H) mm | Full Color | Customer Specific Code |
|                    | 4:                            |            |                        |
|                    | 4-Pin Version                 |            |                        |

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#### Features

- 1. 5050 with integrated high quality constant current IC and RGB LED chip.
- 2. Built-in IC, with high precision of constant current and internal RGB chips spectral processing in advance.
- 3. Single line data transmission (return to zero code) .
- 4. Specific Shaping Transmit Technology number of LED stacked is not restricted.
- 5. Cascading Enhancement Technology any 2 LED spacing can be up to 10 meters
- 6. Data transfer rate of 800 kbp/s at 30 frames per second.
- 7. RGB output port PWM control can achieve 256 grey level adjustments
- 9. Upon powering up, IC performs self-inspection then lights connection on the pin B lamp.
- 10. SA-I Anti-interference patent technology for single line data transmission.
- 11. Built-in power supply reverse connect protection module, reversed power input will not damage the IC.

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# LED Characteristics

|                |                 |                          | (T <sub>a</sub> =-25°C | C, unless otherwise specified) |  |  |  |  |
|----------------|-----------------|--------------------------|------------------------|--------------------------------|--|--|--|--|
| Light<br>color | Wavelength (nm) | Light<br>intensity (mcd) | Working current (mA)   | Working voltage (V)            |  |  |  |  |
| R              | 620-625         | 400-700                  | 12                     | 1.8-2.2                        |  |  |  |  |
| G              | 520-525         | 1000-1500                | 12                     | 2.8-3.2                        |  |  |  |  |
| В              | 465-470         | 200-400                  | 12                     | 2.8-3.2                        |  |  |  |  |

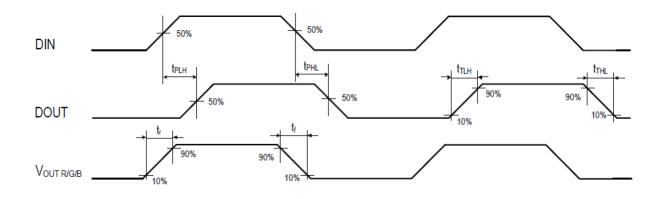
| Recommended Operating Ranges                              |        |         |      |         |      |                 |
|---|--------|---------|------|---------|------|-----------------|
| $(T_a = -25^{\circ}C, \text{ unless otherwise spectrum})$ |        |         |      |         |      |                 |
| Parameter   | Symbol | Min.    | Тур. | Max     | Unit | Test conditions |
| Supply voltage  | VDD    | -       | 5.2  | -       | V    | -               |
| High level input voltage                                  | VIH    | 0.7*VDD | -    |         | V    | VDD=5.0V        |
| Low level input voltage                                   | VIL    | -       | -    | 0.3*VDD | V    | VDD=5.0V        |
| The frequency of PWM                                      | FPWM   | -       | 1.0  | -       | KHZ  | -               |
| Static power consumption                                  | IDD    | -       | 0.5  | -       | mA   | -               |

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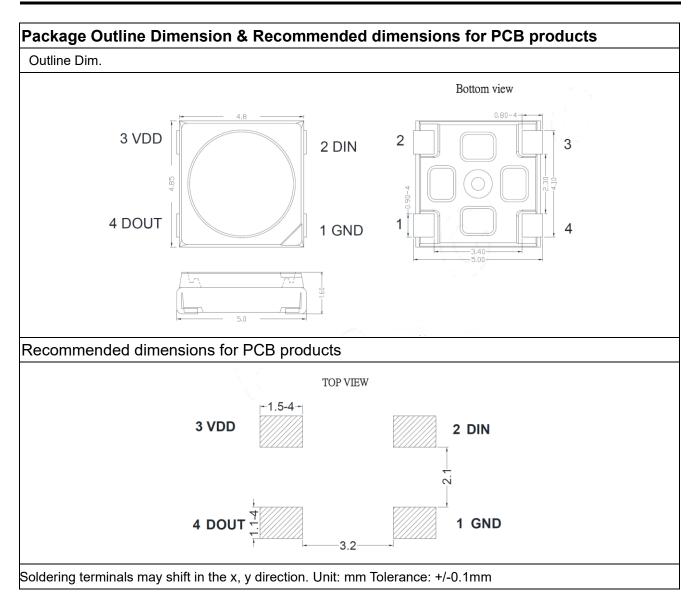
# Switching Characteristics

| (T <sub>a</sub> =-25°C, unless otherwise specifie |        |     |         |     |      |  |  |
|---|--------|-----|---------|-----|------|--|--|
| Parameter   | Symbol | Min | Typical | Max | Unit | Test conditions  |  |
| The speed of data<br>transmission                 | fDIN   |     | 800     |     | KHZ  | The duty ratio of 67%<br>(data 1)  |  |
|   | TPLH   |     | 67      |     | ns   | The earth load   |  |
| DOUT transmission<br>delay                        | TPHL   |     | 82      |     | ns   | capacitance of the<br>dout port is 30pf, and<br>the signal transmission<br>delay from DIN to<br>dout |  |
| Out R/B conversion                                | Tr     |     | 22      |     | ns   | IOUT R / B= 5mA, out<br>R / B port connected<br>with 200 Ω resistor to                               |  |
| time  | Tf     |     | 75      |     | ns   | VDD in series, load<br>capacitance to<br>ground  |  |
| Out G conversion<br>time                          | Tr     |     | 18      |     | ns   | IOUT g = 5mA, out g<br>port is connected<br>with 200 Ω resistor to<br>VDD in series, and the         |  |
|   | Tf     |     | 110     |     | ns   | load capacitance to<br>ground is 30pf  |  |



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#### **PIN Description**

| Number | Imber Symbol Function Description |                            |  |
|--------|-----------------------------------|----------------------------|--|
| 1      | GND                               | Ground                     |  |
| 2      | DIN                               | Control data signal input  |  |
| 3      | VDD                               | Power Supply LED           |  |
| 4      | DOUT                              | Control data signal output |  |

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#### Absolute Maximum Ratings

| $r_a = 25 \text{ °C}, v_{ss} = 0 \text{ v}, \text{ unless otherwise specified}$ |        |              |      |  |  |  |
|---|--------|--------------|------|--|--|--|
| Parameter   | Symbol | Range        | Unit |  |  |  |
| Logic supply voltage  | Vdd    | +3.7~+5.5    | V    |  |  |  |
| Logic input voltage   | Vın    | -0.5~Vdd+0.5 | V    |  |  |  |
| Operating temperature   | Торт   | −40 to +85   | °C   |  |  |  |
| Storage temperature   | Тѕтб   | −40 to +85   | °C   |  |  |  |
| ESD pressure  | Vesd   | 2K           | V    |  |  |  |

#### $(T_a = 25 \circ C, V_{ss} = 0V, unless otherwise specified)$

#### **Functional Description**

The IN-PI554FCH sends signals in return to zero codes with a single-wire communication method.

When the power-on reset is completed, the IN-PI554FCH receives the data from the DIN pin.

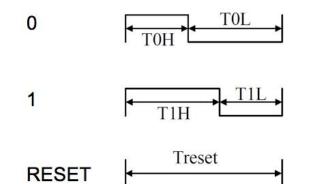
When all the 24 bits of data have been received, IC no longer receive data, the DOUT port starts to forward the data to the next chip as its input data. The DOUT pin is held LOW before the data forwarding. The three PWM output ports, OUTR, OUTG and OUTB, drive Duty ratio output in a 0.6-ms period corresponding to the 24-bit data received before. If the input data from the DIN pin is a RESET code, the IN-PI554FCH will drive the newest received 24-bit data for display. When the reset code is completed, the IN-PI554FCH will start receive the new 24-bit data. When 24 bits of data have been received, the IN-PI554FCH will forward the data through the DOUT pin. Before the RESET signal is received, the output at the OUTR, OUTG and OUTB pins will remain unchanged. When a low level RESET code longer than 80µs is received, the IN-PI554FCH will drive Duty ratio output corresponding to the newest 24-bit data received The IN-PI554FCH employs an automatic shaping-forwarding technique, so the number of the cascaded chips is not limited by the signal transfer, and is only limited by the panel refresh speed. For example, in a 1024-chip cascaded design with the panel refresh time of 1024X3X8 X 1.25 (us) =30ms, no flickering will appear.

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# **Timing Waveforms**

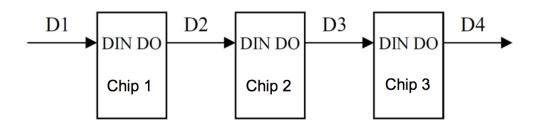
1. Input Code



2. The data transmission time (TH+TL=1.25µs±600ns):

|      | Name                         |      | Name |         | Standard<br>value | Max. | Unit |
|------|------------------------------|------|------|---------|-------------------|------|------|
| Т    | Code period                  | 1.20 |      | -       | μs                |      |      |
| тон  | 0 code, high level time      | 0.20 | 0.30 | 0.40    | μs                |      |      |
| TOL  | 0 code, low level time       | 0.80 |      |         | μs                |      |      |
| T1H  | 1 code, high level time      | 0.70 | 0.90 | 1.00    | μs                |      |      |
| T1L  | 1 code, low level time       | 0.20 |      | <u></u> | μs                |      |      |
| Trst | Reset code,low level<br>time | 200  |      |         | μs                |      |      |

3. Connection Scheme



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4. Data Transfer Format

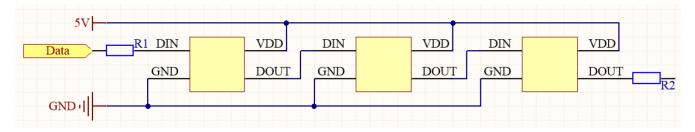
|                     | Î            |               | F            | Reset code | 2                    |               | reset        | code |
|---------------------|--------------|---------------|--------------|------------|----------------------|---------------|--------------|------|
|                     | Data         | a refresh cy  | cle 1        | 200        | Data refresh cycle 2 |               |              |      |
| D1                  | first 24 bit | second 24 blt | third 24 bit |            | first 24 bit         | second 24 blt | third 24 bit |      |
|                     |              |               |              |            |                      |               |              |      |
| D2 _                |              | second 24 blt | third 24 bit |            |                      | second 24 blt | third 24 bit |      |
|                     |              |               |              |            |                      |               |              |      |
| D3                  |              |               | third 24 bit |            |                      |               | third 24 bit | n    |
| D4                  |              |               |              |            |                      |               |              |      |
| <i>L</i> , <u> </u> |              |               | 3            |            |                      |               |              |      |

#### 5. 24-bit data format

| G7 | G6 | G5        | G4 | G3        | G2        | G1 | G0        | <b>R7</b> | R6        | R5 | R4 |
|----|----|-----------|----|-----------|-----------|----|-----------|-----------|-----------|----|----|
| R3 | R2 | <b>R1</b> | RO | <b>B7</b> | <b>B6</b> | B5 | <b>B4</b> | <b>B3</b> | <b>B2</b> | B1 | BO |

Note: high starting, in order to send data (G7 - G6 - ........B0)

# **Typical Application circuit diagram**

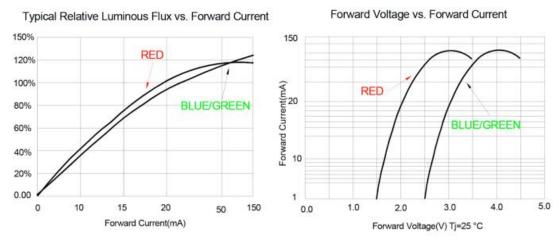


Application: used for soft lamp strip or hard light, lamp beads transmission distance is short, suggested in signal in time the clock line input and output end of each connected in series protection resistors, R1=R2 of about 500 ohms.

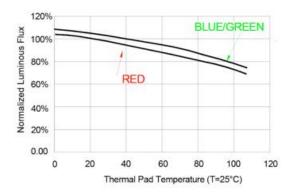
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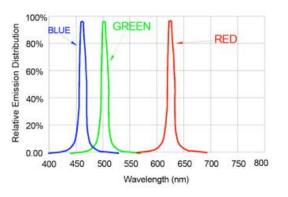
# **LED Performance Graph**



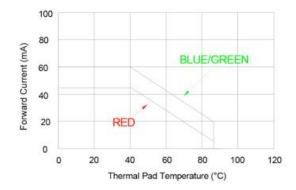
Thermal Pad Temperature vs. Relative Light Output



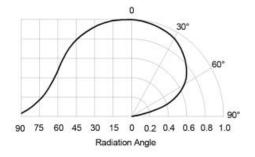
Wavelength Characteristics



Thermal Pad Temperature vs. Forward Current



Typical Radiation Pattern 120°



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#### Precautions

Please read the following notes before using the product:

- 1. Storage
- 1.1 Do not open moisture proof bag before the products are ready to use.
- 1.2 Before opening the package, the LEDs should be kept at 30  $^\circ\!\!\mathbb{C}$  or less and 80%RH or less.
- 1.3 The LEDs should be used within a year.
- 1.4 After opening the package, the remaining LEDs should be kept in a resealed bag.
- 1.5 The LEDs require mandatory baking before usage. Baking treatment listed below.
- 1.6 If the moisture adsorbent material has fabled away or the LEDs have exceeded the storage time, baking treatment should be performed using the following conditions.

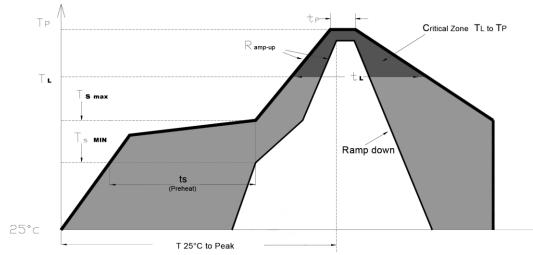
\*Baking treatment:  $60\pm5^{\circ}$ C for 24 hours.

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# 2. Soldering Condition

### Recommended soldering conditions:



Times

| Profile Feature                                      | Lead-Free Solder |
|--|------------------|
| Average Ramp-Up Rate (Ts <sub>max</sub> to Tp )      | 3°C/second max.  |
| Preheat: Temperature Min (Ts <sub>min</sub> )        | 150°C            |
| Preheat: Temperature Min (Ts <sub>max</sub> )        | 200°⊂            |
| Preheat: Time ( ts min to ts max )                   | 60-180 seconds   |
| Time Maintained Above: Temperature (T <sub>L</sub> ) | 217 ℃            |
| Time Maintained Above: Time (t $_{L}$ )              | 60-150 seconds   |
| Peak/Classification Temperature (T P)                | <b>240</b> ℃     |
| Time Within 5°C of Actual Peak Temperature ( tp)     | <10 seconds      |
| Ramp-Down Rate                                       | 6°C/second max.  |
| Time 25 $^\circ\!C$ to Peak Temperature              | <6 minutes max.  |

Note: Excessive soldering temperature and / or time might result in deformation of the LED lens or catastrophic failure of the LED.

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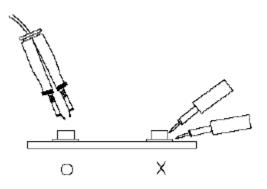


# 3. Soldering Iron

Each terminal is to go to the tip of soldering iron temperature less than  $260^{\circ}$  for 5 seconds within once in less than the soldering iron capacity 25W. Leave two seconds and more intervals, and do soldering of each terminal. Be careful because the damage of the product is often started at the time of the hand solder.

# 4. Repairing

Repair should not be done after the LEDs have been soldered. When repairing is unavoidable, a double-head soldering iron should be used (as below figure). It should be confirmed beforehand whether the characteristics of the LEDs will or will not be damaged by repairing.



# 5. Caution in ESD

Static Electricity and surge damages the LED. It is recommended to use a wristband or antielectrostatic glove when handling the LED. All devices, equipment and machinery must be properly grounded.

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# **Revision History**

| Changes since last revision                               | Page   | Version No. | Revision Date |
|---|--------|-------------|---------------|
| Initial release   | -      | 1.0         | 12-21-2015    |
| Update optical electrical characteristics                 |        | 2.0         | 06-10-2016    |
| Update data transmission time / intensity level/ handling |        | 2.1         | 10-20-2016    |
| Update intensity level                                    |        | 2.2         | 10-31-2016    |
| Update intensity level                                    |        | 2.3         | 01-07-2019    |
| Revise the drawing; revise the precaution.                | 14, 16 | 2.4         | 07-31-2019    |
| Revise the drawing  | P7~P12 | 2.5         | 02-04-2021    |
|   |        |             |               |
|   |        |             |               |

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